

FIG. 1

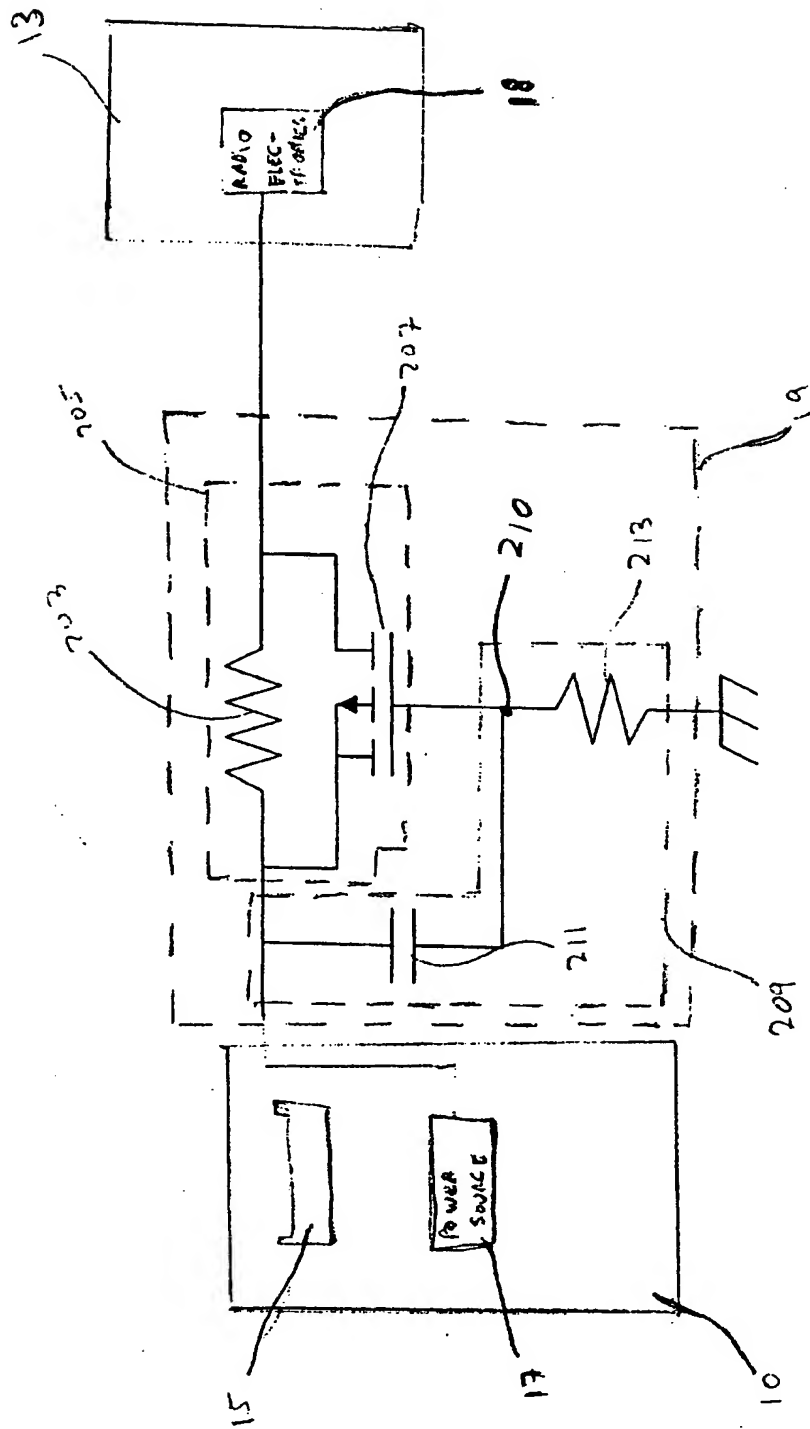


FIG. 2

Type I A (CF cond)

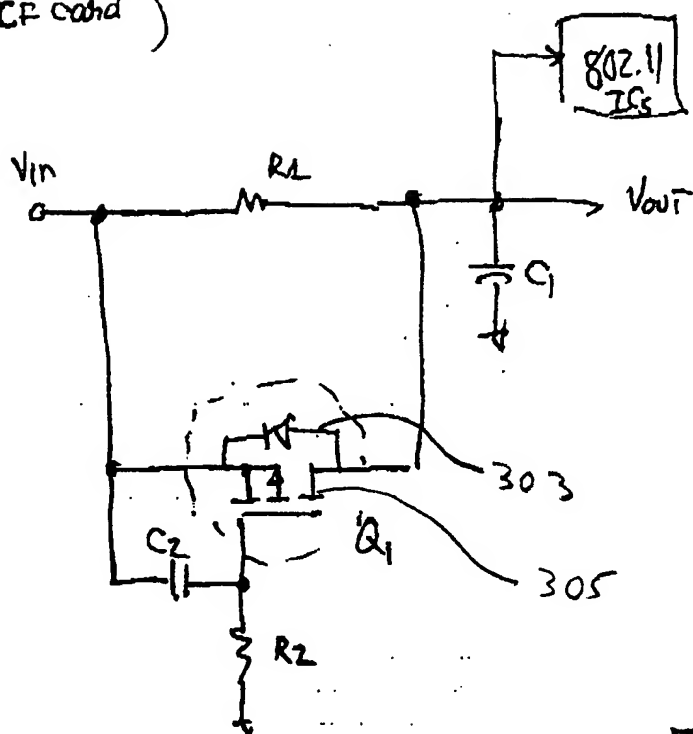


FIG. 3

TYPE II A

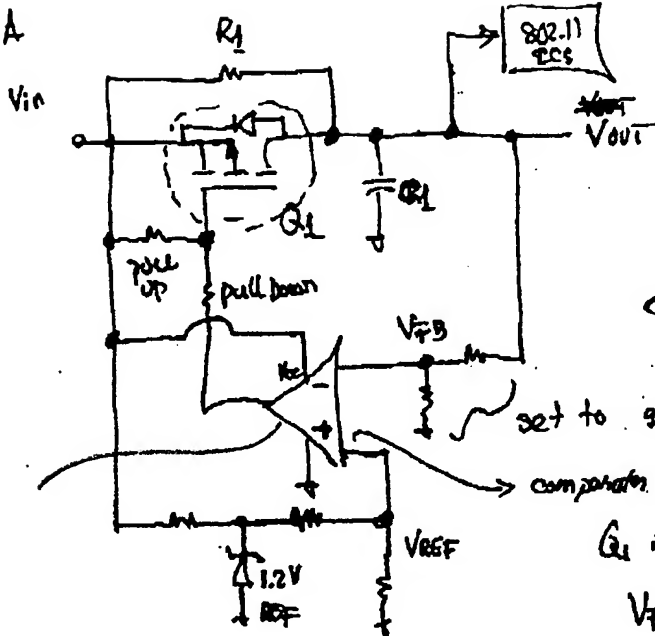


FIG 4. A

Q_1 is on when $V_{FB} > V_{REF}$ 90-95
 V_{FB} is set point when V_{OUT} is 95% V_{in}

TYPE III A

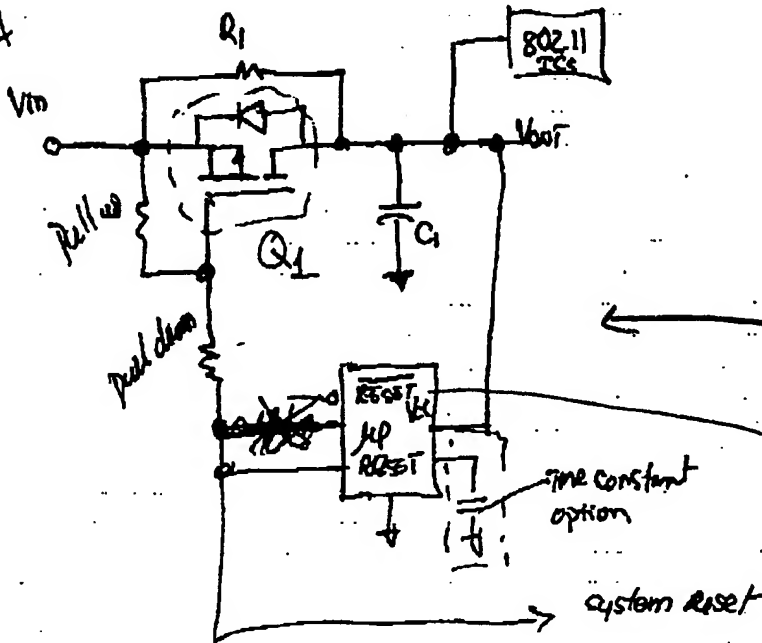
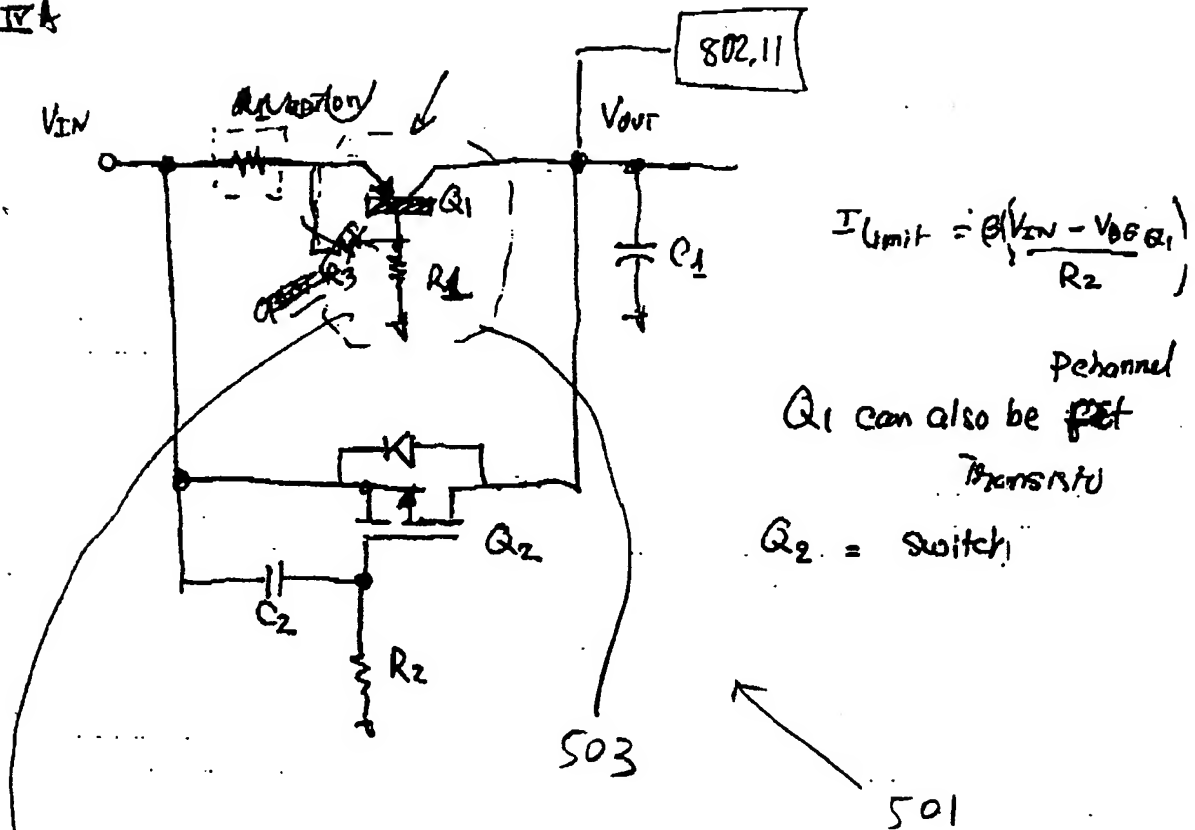


FIG. 4B

TYPE IV A



$$I_{limit} = \beta \left(\frac{V_{IN} - V_{DS Q1}}{R_2} \right)$$

p-channel
Q1 can also be ~~p~~ FET
Transistor

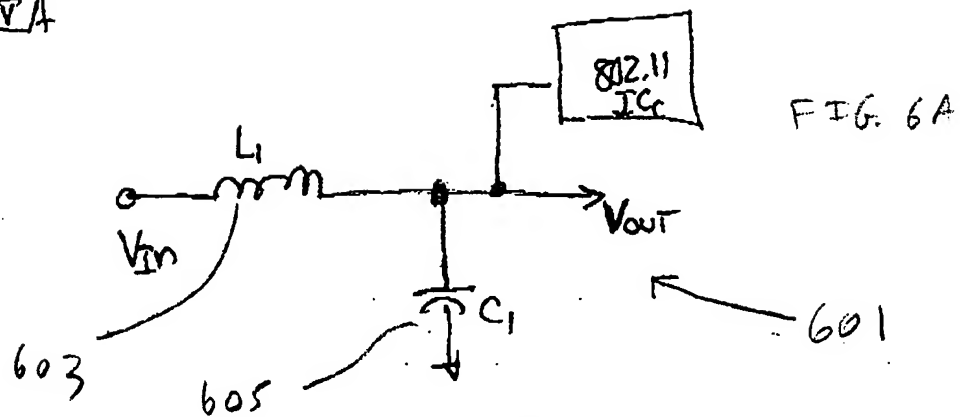
Q2 = switch

could be

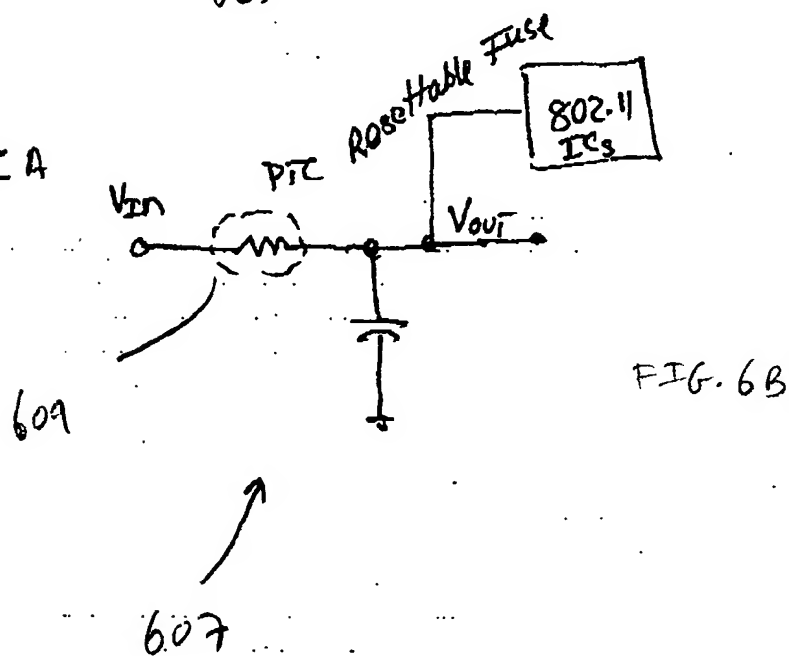
- Voltage control Resistor
- Current regulator diode
- ~~Regulator~~ ^{p-channel} FET Transistor Q1

FIG. 5

Type ~~VI~~ A



Type VI A



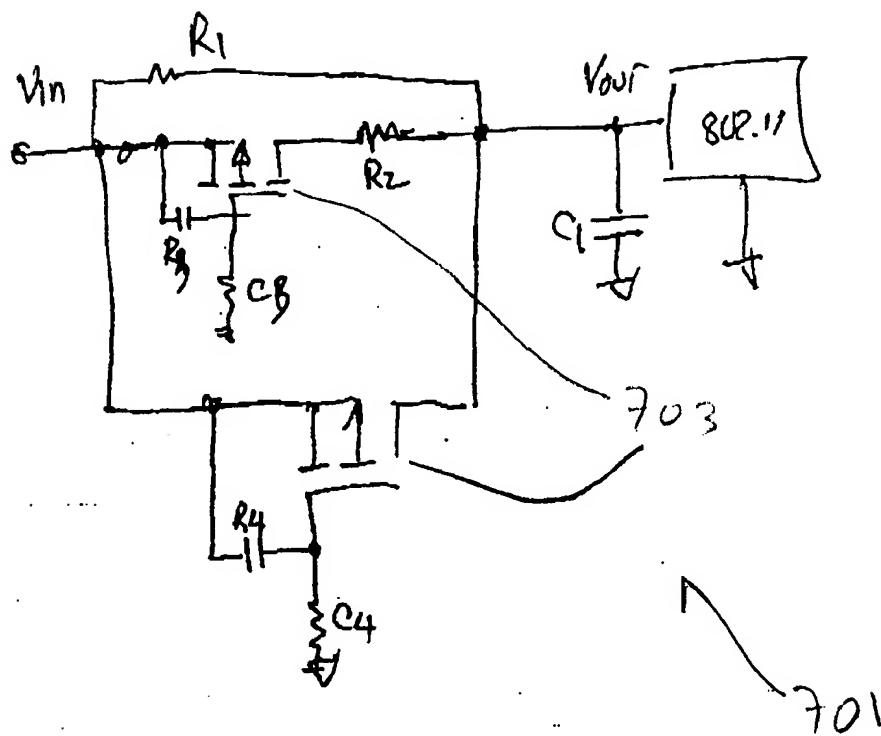


FIG. 7

Type I B

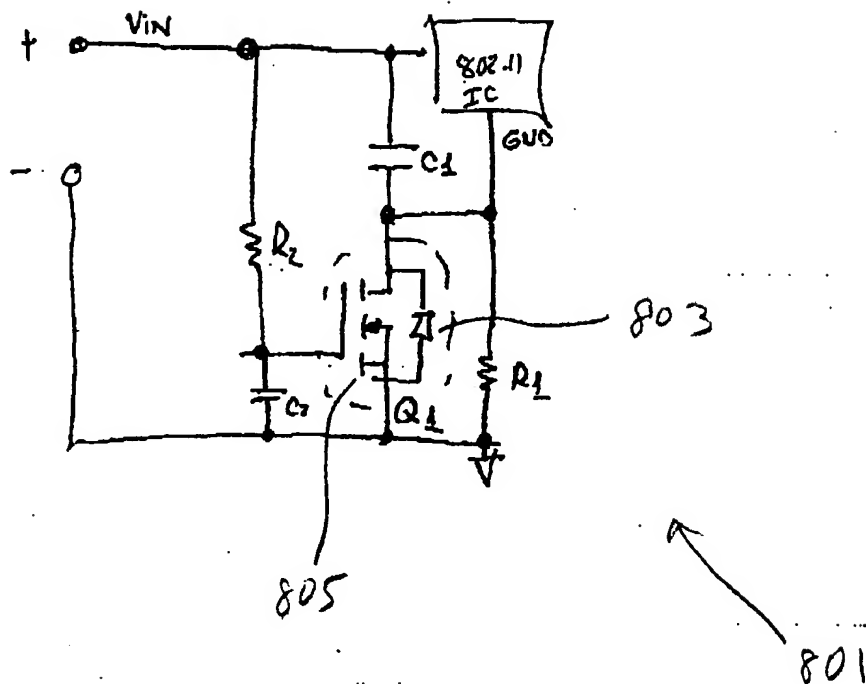


FIG. 8

Type II B

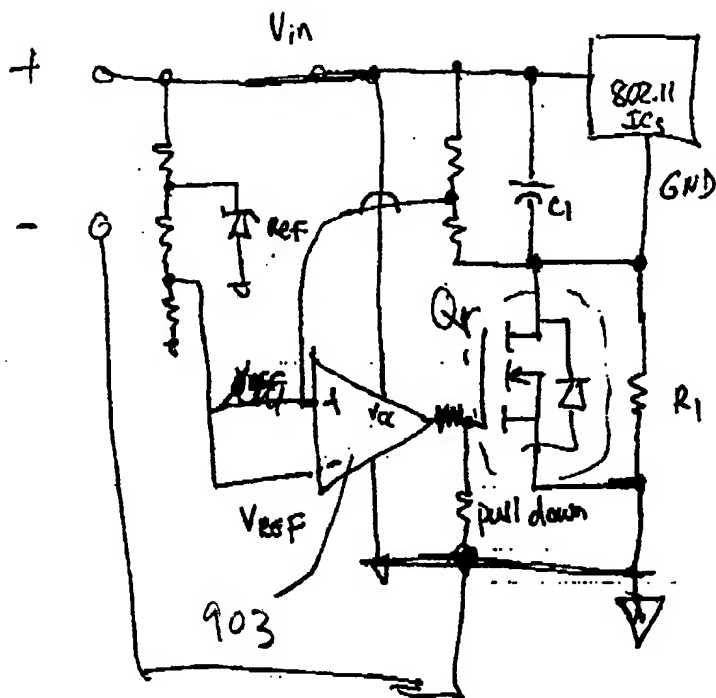


FIG 9A

Type III B

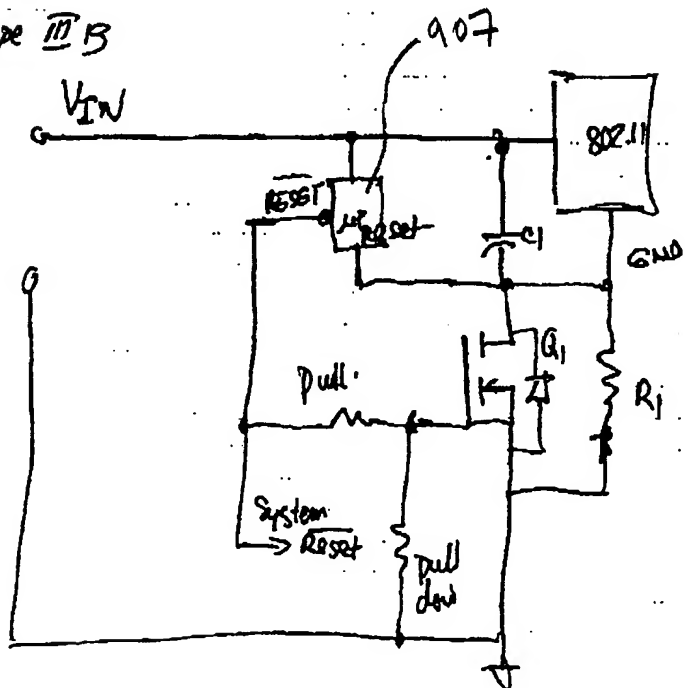
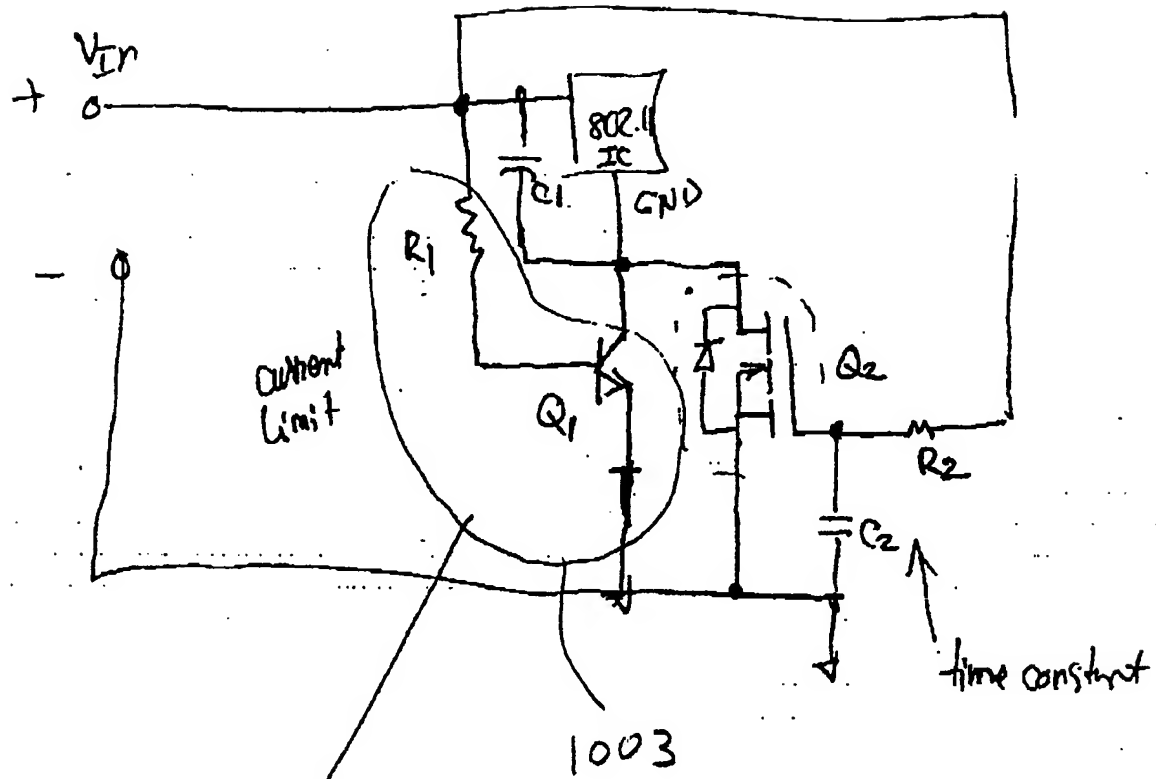


FIG. 9B

Type IV B



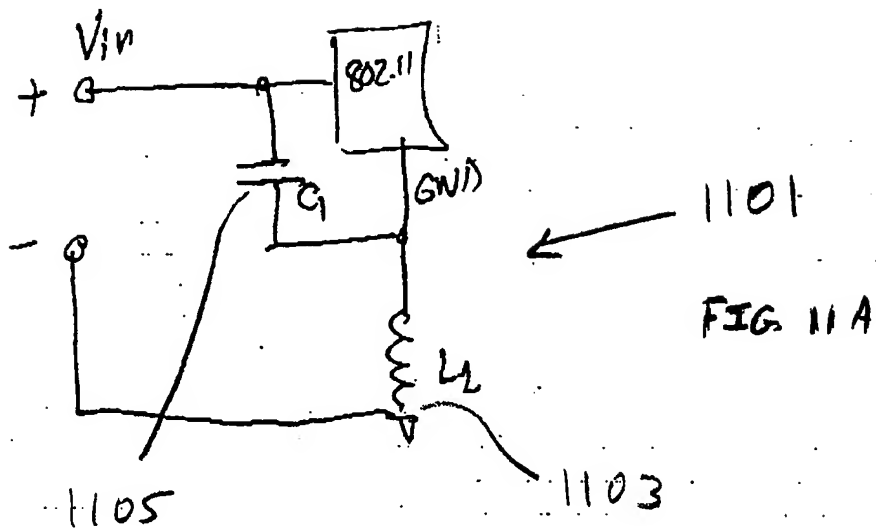
could be

- Voltage control resistor
- Output regulation diode
- N channel FET (Q_1) transistor

1001

FIG. 10

TYPE V B



TYPE VI B

